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501.33873VC4

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: KAWAGOE, et al.

Filed: December 14, 2001

For: PROCESS FOR MANUFACTURING A
SEMICONDUCTOR WAFER, A SEMICONDUCTOR
WAFER, PROCESS FOR MANUFACTURING A
SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE,
AND SEMICONDUCTOR INTEGRATED CIRCUIT
DEVICE



CLAIM FOR PRIORITY

Assistant Commissioner for Patents
Washington, D.C. 20231

December 14, 2001


Sir:

Under the provisions of 35 USC §119 and 37 CFR §1.55, Applicants hereby claim the right of priority based on Japanese Priority Application No. 6-265529, filed on October 28, 1994 and No. 6-176872, filed on July 28, 1994, each in Japan.

Certified copies of the two (2) above-referred-to Japanese Priority Applications were filed in application Serial No. 08/508,483, filed July 28, 1995, on July 28, 1995.

Respectfully submitted,

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